

Configuration 1

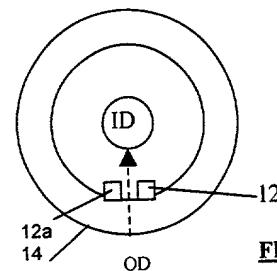


FIG. 1B

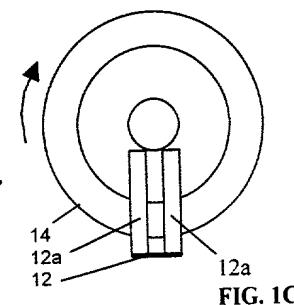
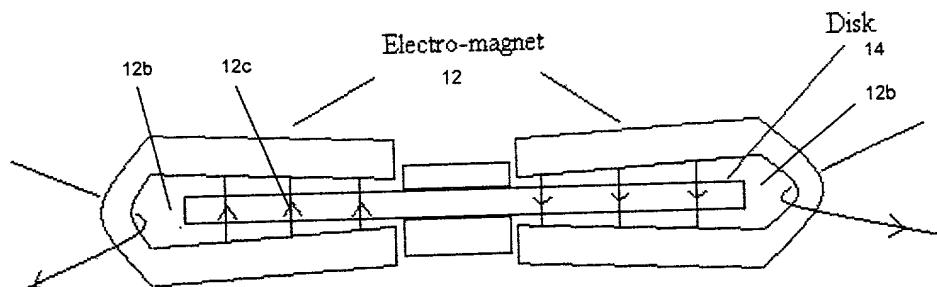


FIG. 1C



Configuration 2

FIG. 2A

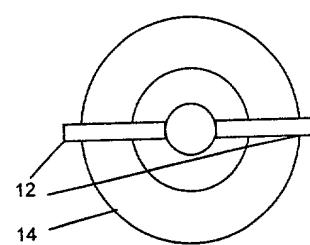


FIG. 2B

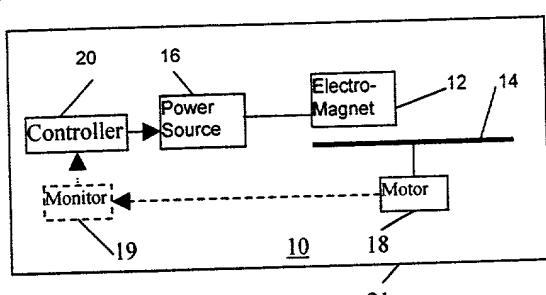


FIG. 2C

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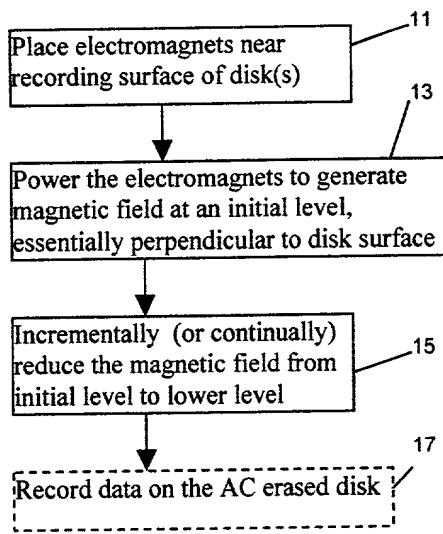


FIG. 1D

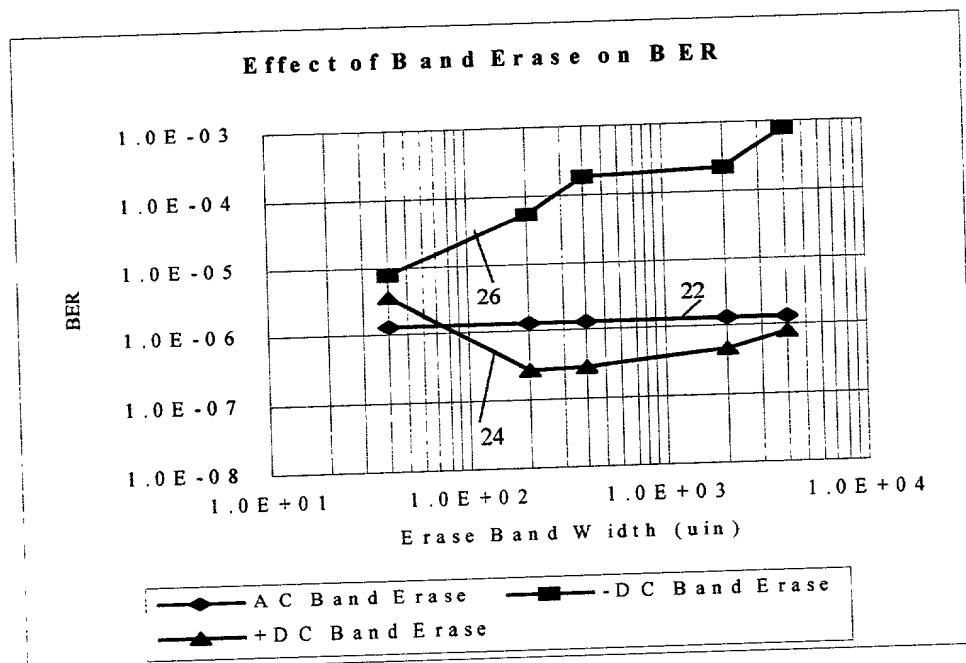


FIG. 3A - Effect of Band Erase on BER

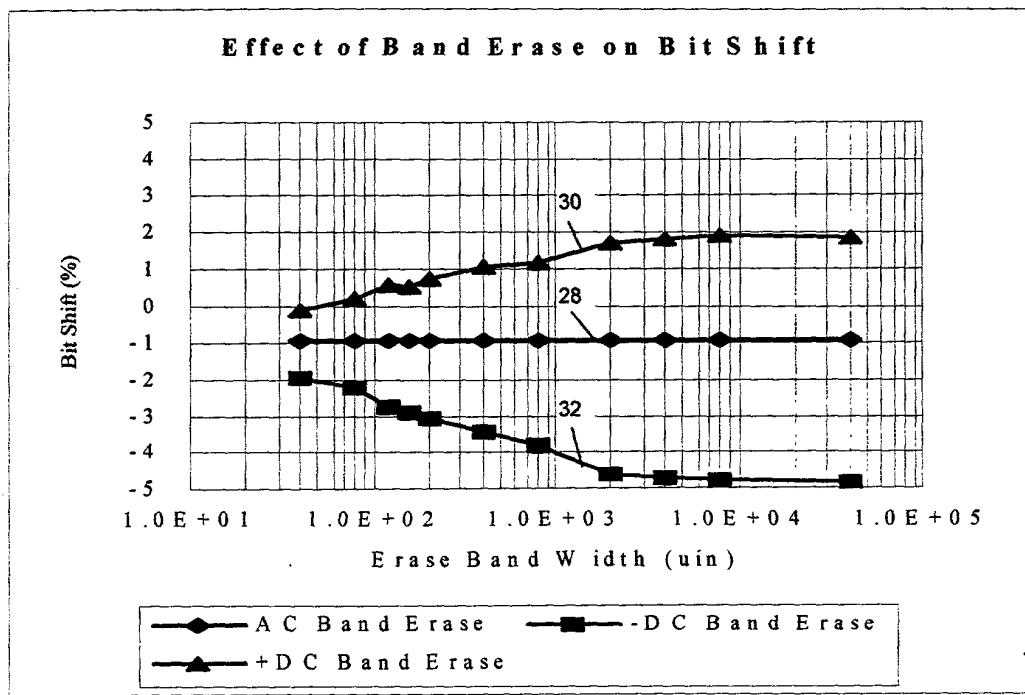


FIG. 3B - Effect of Band Erase on Transition Shift

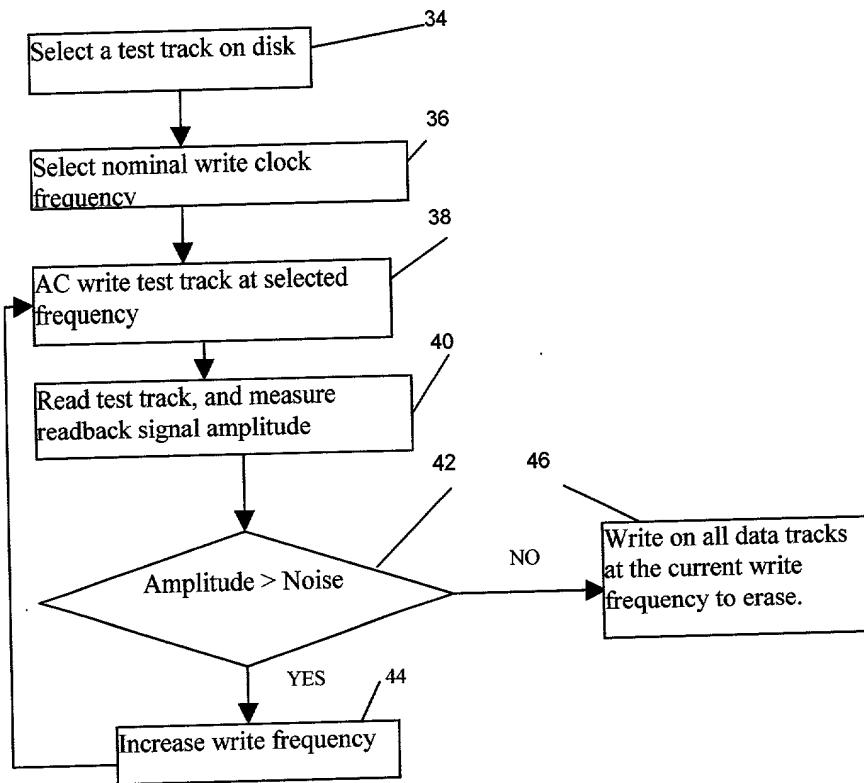


FIG. 4A

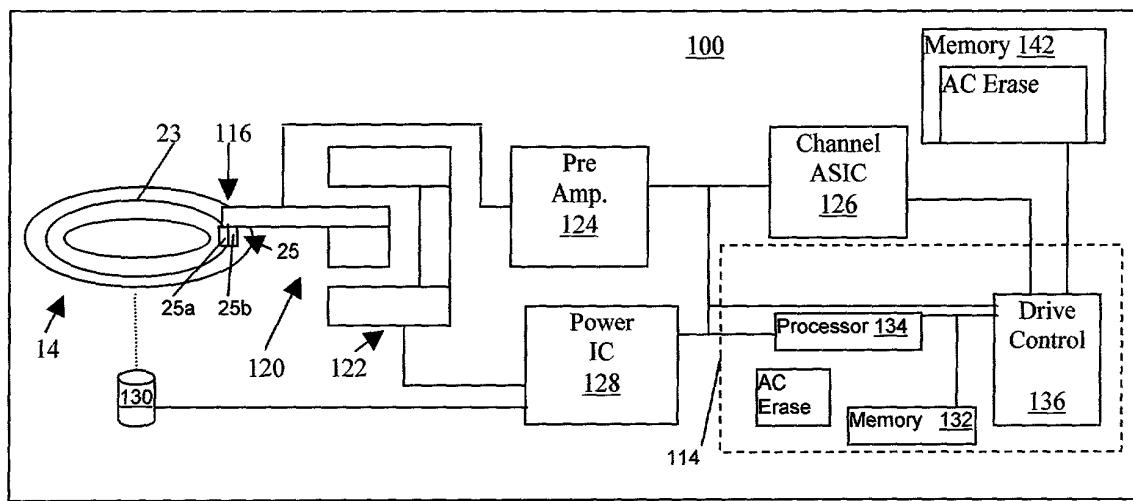


FIG. 4B

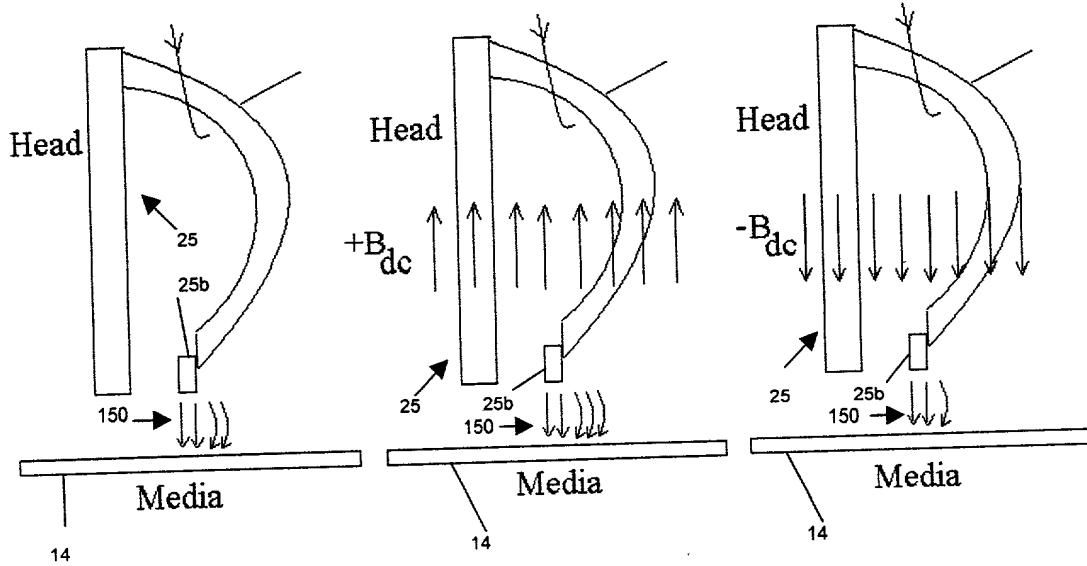


FIG. 4C (AC Erase)

FIG. 7B (+DC Erase)

FIG. 7C (-DC Erase)

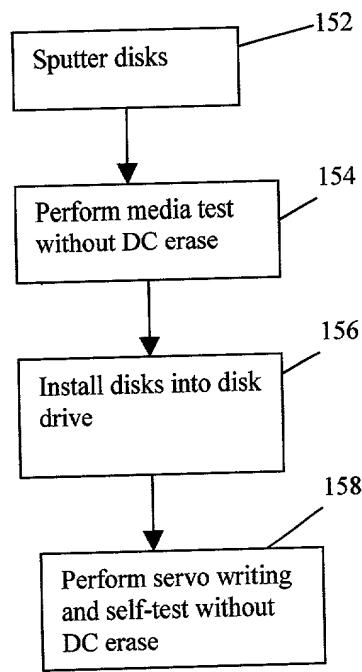


FIG. 5

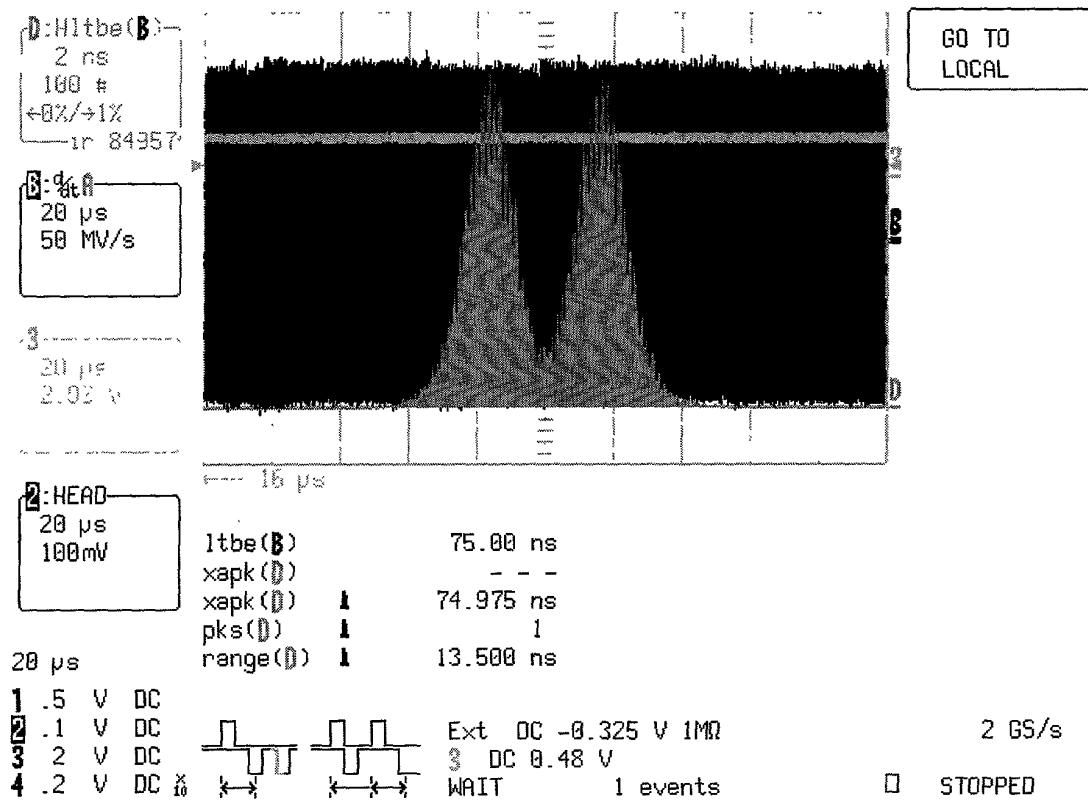


FIG. 6A (Prior Art) - Readback signal timing histogram of differentiated data written after a conventional DC band erase.

PCB-PCB-PCB-PCB-PCB-PCB

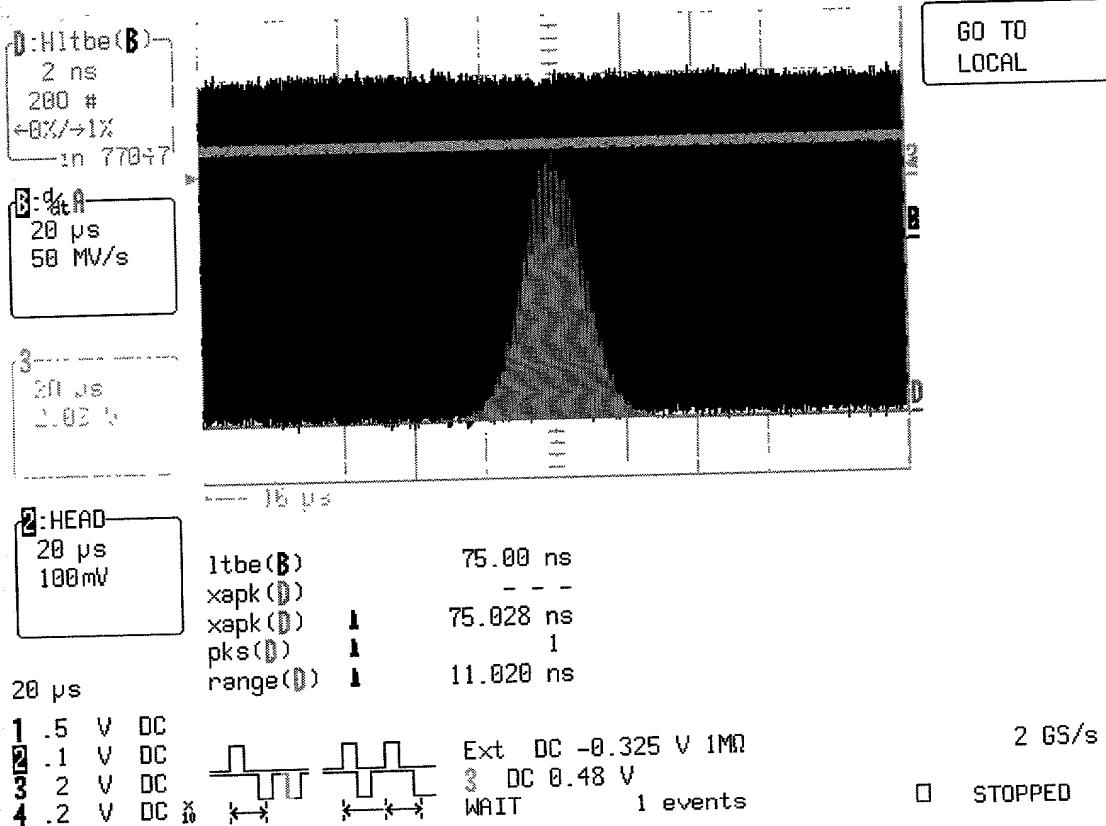


FIG. 6B - Read back signal measurement for a track written on as-received media from disk sputtering process without any net magnetization, wherein timing asymmetry is eliminated.

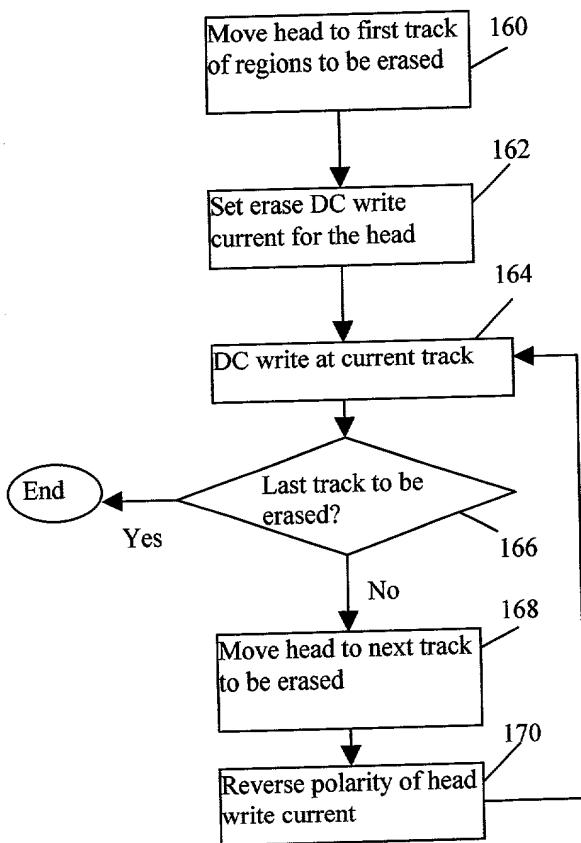


FIG. 7A

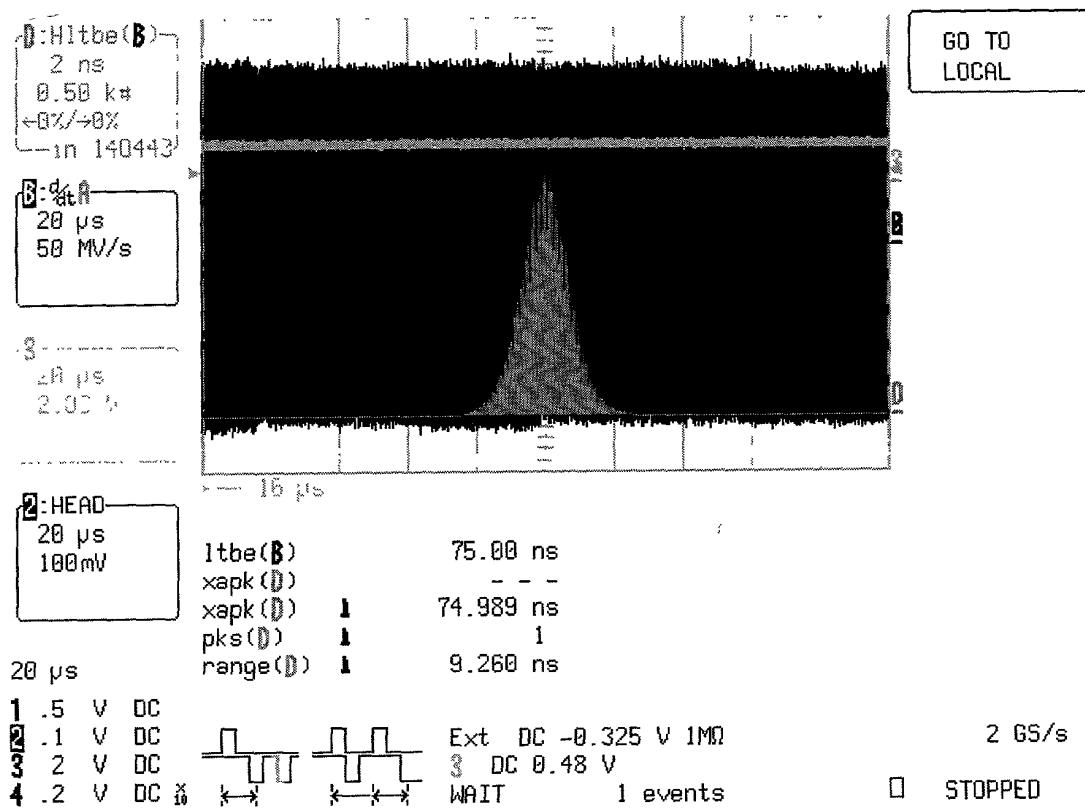


FIG. 7D - Measurement of data written on media preconditioned by DC erasing with alternate polarity on adjacent tracks, wherein timing asymmetry is eliminated.